

Attorney's Docket No. MP0237/13361/049001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Sehat Sutardja
Serial No. : 10/621,058
Filed : July 15, 2003
Title : LOW LOSS DC/DC CONVERTER

Art Unit: Unknown
Examiner: Unknown

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

Each enclosed document listed on the herewith Form PTO-1449 was cited in the attached European Search Report dated December 1, 2004, issued in a foreign counterpart application not more than three months ago. To the best knowledge of the undersigned, this is the first citation of these items in any communication from a foreign patent office in a counterpart foreign application.

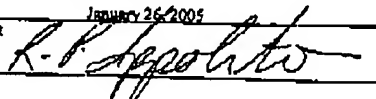
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Attorney's Docket No. MP0237/13361/049001

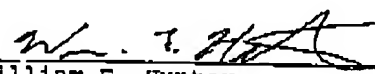
The documents are in the English language, hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 01/26/05


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Date February 10, 2005

To Examiner Jeffrey L. Sterrett
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Group Art Unit 2838
Telephone: 571-272-2085

Facsimile number 13361-04900001 / 571-273-2085

From William E. Hunter

Re LOW LOSS DC/DC CONVERTER
Applicant: Sehat Sutardja
Application No.: 10/621,053
Filing Date: July 15, 2003
Country: United States
Your Ref.: MP0237
Our Ref.: 13361-049001

Number of pages
including this page 46

Message Attached is a copy of the Information Disclosure Statement filed January 26, 2005.

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Sheet 1 of 1

| | | | |
|--|--|---|--------------------------------------|
| Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b)) | U.S. Department of Commerce Patent and Trademark Office | Attorney's Docket No. MP0237/13361/049001 | Application No. 10/621,058 |
| | Applicant Sehat Sutardja | | |
| | Filing Date July 15, 2003 | Group Art Unit | |

| U.S. Patent Documents | | | | | | | |
|-----------------------|-----------|-----------------|------------------|-------------|-------|----------|----------------------------|
| Examiner Initial | Desig. ID | Document Number | Publication Date | Patentee | Class | Subclass | Filing Date if Appropriate |
| | AA | 5,010,261 | 04/23/1991 | Stelgerwald | | | |
| | AB | 5,687,067 | 11/11/1997 | Majid et al | | | |
| | AC | | | | | | |
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| | AE | | | | | | |
| | AF | | | | | | |
| | AG | | | | | | |
| | AH | | | | | | |
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| | AJ | | | | | | |
| | AK | | | | | | |

| Foreign Patent Documents or Published Foreign Patent Applications | | | | | | | | |
|---|-----------|-----------------|------------------|--------------------------|-------|----------|---------------|----|
| Examiner Initial | Desig. ID | Document Number | Publication Date | Country or Patent Office | Class | Subclass | Translation | |
| | AL | 08-182305 | 07-12-1996 | Japan | | | Yes | No |
| | AM | | | | | | Abstract Only | |
| | AN | | | | | | | |
| | AO | | | | | | | |
| | AP | | | | | | | |

| Other Documents (Include Author, Title, Date, and Place of Publication) | | |
|---|-----------|--|
| Examiner Initial | Desig. ID | Document |
| | AQ | Scutt Deary, "Optimizing Transistor Performance in Synchronous Rectifier, Buck Converters", IEEE, 2000 |
| | AR | |
| | AS | |
| | AT | |

| | |
|---|-----------------|
| Examiner Signature | Date Considered |
| EXAMINER: Initial date considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | |
| Substitute Disclosure Form (PTO-1449) | |



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Département 4
La Haye
Division de la
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Grünecker, Kinkeldey,
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68, Dez. 2004

Datum/Date

01.12.04

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|--|---|
| Zeichen/Ref./Ref. EP28639-034/Peu | Anmeldung Nr./Application No./Demande n°/Patent No./Brevet n° 03024437.0-2207- |
| Anmelder/Applicant/Demandeur/Pateninhaber/Proprietor/Titulaire Marvell World Trade Ltd. | |

COMMUNICATION

The European Patent Office herewith transmits as an enclosure the European search report for the above-mentioned European patent application.

If applicable, copies of the documents cited in the European search report are attached.

☒ Additional set(s) of copies of the documents cited in the European search report is (are) enclosed as well.

The following specifications given by the applicant have been approved by the Search Division:

☐ abstract ☒ title

☒ The abstract was modified by the Search Division and the definitive text is attached to this communication.

The following figure will be published together with the abstract: 2

REFUND OF THE SEARCH FEE

If applicable under Article 10 Rules relating to fees, a separate communication from the Receiving Section on the refund of the search fee will be sent later.



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 03 02 4437

| DOCUMENTS CONSIDERED TO BE RELEVANT | | | |
|---|---|--|--|
| Category | Citation of document with indication, where appropriate, of relevant passages | Relevant to claim | CLASSIFICATION OF THE APPLICATION (Incl. 17) |
| A | PATENT ABSTRACTS OF JAPAN vol. 1996, no. 11, 29 November 1996 (1996-11-29) -A JP 08 182305 A (MATSUSHITA ELECTRIC WORKS LTD), 12 July 1996 (1996-07-12) * abstract; figures 1,3,16 * | 1-36 | H03K17/16 H02M3/158 H03K17/687 |
| A | US 5 687 067 A (MAJID NAVEED ET AL) 11 November 1997 (1997-11-11) * column 3, line 52 - column 4, line 65; figures 2-4 * | 1-36 | |
| T | US 5 010 261 A (STEIGERWALD ROBERT L) 23 April 1991 (1991-04-23) * column 3, line 36 - column 3, line 50 * | | |
| T | DEUTY S: "Optimizing transistor performance in synchronous rectifier buck converters" APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 2000. APEC 2000. FIFTEENTH ANNUAL IEEE NEW ORLEANS, LA, USA 6-10 FEB. 2000, PISCATAWAY, NJ, USA, IEEE, US, 6 February 2000 (2000-02-06), pages 675-678, XP010371606 ISBN: 0-7803-5864-3 * page 676 * | | |
| | | | TECHNICAL FIELDS SEARCHED (Incl. 17) |
| | | | H03K H02M |
| The present search report has been drawn up for all claims | | | |
| Place of search Madrid | | Date of completion of the search 23 November 2004 | Examiner Braccini, R |
| CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background Q : non-written disclosure P : intermediate document I : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons A : member of the same patent family, corresponding document | | | |

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 03 02 4437

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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23-11-2004

| Patent document cited in search report | | Publication date | Patent family member(s) | Publication date |
|---|---|---------------------|----------------------------|---------------------|
| JP 08182305 | A | 12-07-1996 | NONE | |
| US 5687067 | A | 11-11-1997 | NONE | |
| US 5010261 | A | 23-04-1991 | NONE | |

EPO FORM P013

For more details about this annex: see Official Journal of the European Patent Office, Nr. 12/82